

**Office of the Dean Research and Consultancy
Indian Institute of Engineering Science & Technology (IEST), Shibpur
Howrah-711 103**

Project on: "Scalable Manufacturing of Wrinkled Graphene FET Array with Fluidic Interface for Trace Exosome Detection Towards Early Cancer Diagnosis"

under the PAIR Network titled "An Eight Institution Partnership for Advancing Materials Research and Development: From Fundamental Design to Emerging Applications in Structural, Functional and Biomaterials Spaces"

[Sponsoring Authority: ANRF, Govt. of India]
Department of Electronic and Telecommunication Engineering
Indian Institute of Engineering Science & Technology (IEST), Shibpur
Howrah-711 103

Ref: Advt. No. CH 1905, dated 06.01.2026

[Institute Project Code: DRC/ANRF-PAIR (4211)/CHEM/PB/010/25-26]

Interested candidates are requested to appear for walk in interview on 15.1.2026 at 1:00 PM, for the following post in the Department of Electronic and Telecommunication Engineering (ETCE), Indian Institute of Engineering Science & Technology (IEST), Shibpur, Howrah-711 103.

Name of the post: Junior Research Fellow

No of post - One (01)

Essential Qualification: M.E./M. Tech. in Biomedical Engineering/Biotechnology/Materials Science/Bioengineering/Nanotechnology/Chemical Engineering with specializations related to sensor devices, electrochemistry or equivalent/Electronics Engineering with Microelectronics specialization or any allied fields /M.Sc. in Biotechnology / Biochemistry / Life Sciences from a recognized University/Institute with minimum 60% marks or 6.5 CGPA on a 10-point scale are eligible to apply for the JRF positions with valid GATE/ NET. For SC/ST/PwD category, a minimum 55% marks or 6.0 CGPA on 10-point scale is required with valid GATE/NET.

Fellowship: Rs. 37,000/- p.m. + 11,100/- (HRA @30%) = Rs.48,100/- per month

Age Limit: Maximum 32 years, (Upper age limit is relaxable upto 5 years for SC/ST/OBC/Woman and Physically handicapped candidates).

Duration of the Project: Five years from the date of commencement or till the project continues whichever is earlier

Interested eligible candidates should mail soft copies of the application letter in plain paper, recent bio-data, mark sheets and certificates. All documents should be self-attested. Physical documents will be verified at the time of joining. The selection will be canceled if any discrepancies are found in the documents at the time of physical verification.

Venue and Date of the interview: Office of the Dean (R&C), IEST, Shibpur on 15.1.2026 at 1:00 PM

Note:

1. Soft copies of the application letter, bio-data, marksheets and certificates should be sent through e-mail in advance by January 14th, 2026 at 5:00 pm to: Prof. Chirasree Roy Chaudhuri, Department of Electronic and Telecommunication Engineering (PI) (E-mail id: chirashree@telecom.iests.ac.in).
2. All applications must mention a valid e-mail id and phone number for communication (if required).
3. Short listing may be done before the interview.

(W. Code DRC-040/25-26)


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